

ON THE SPEED OF COMPUTERS WITH PAGED AND INTERLEAVED MEMORY

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Abstract:

A performance measure (the speed) of computer mathematical models is defined. This measure is given as a function of hardware and program behaviour for Bélády's computer model with paged memory and Volihiman's model with interleaved memory.

KEY WORDS AND PHRASES: computer systems performance, demand paging, interleaved memory behaviour.

1. Introduction

Computer performance is investigation by empirical, simulation and analytical methods [1].

The analytical methods is based on the analysis of mathematical by "exact" methods (e.g. queueing or Markov-chain theory, combinatorics etc.).

Due to the inaccuracy of models the analytical method usually gives only a rough estimate, but the results are general and convenient for computer planning or development.

In this lecture we recommend and analytical method, based on Bélády's [2], Coffman's [3] and Kogan's [4] methods and give some concrete formulas derived by this method.

2. Definition of the speed

The set $N = \{v_1, \dots, v_n\}$ ($1 \leq n < \infty$) is called a program, and the sequence $\omega_T = r_1 \dots r_T$ ($1 \leq T \leq \infty$, $r_t \in N$, $t = 1, \dots, T$) consisting of elements of N . (T -elements permutations with repetition) is called a program realization of length T . Denote N^T the set of all possible sequences ω_T . Denote $\tau[\omega_T]$ the processing time of a sequence ω_T on given computer model. The distribution of the elements of N in the sequence ω_T is called program behaviour [5]. This behaviour is given by the set $D = \{D_1, \dots, D_T\}$ of distribution function D_1, \dots, D_T where $D_T[\omega_T]$ gives the probability of ω_T in the space of events N^T , that is

$$(2.1.) \quad \forall \omega_T \quad 0 \leq D_T[\omega_T] \leq 1$$

and

$$(2.2.) \quad \forall_T \quad \sum_{\omega_T \in N^T} D_T[\omega_T] = 1.$$

Further we suppose

$$(2.3) \quad \sum_{i=1}^n D_{T+1}[\omega_T v_i] = D_T[\omega_T].$$

Instead of $D_T[\omega_T]$ we use the marking $D[\omega_T]$. Denote the set of D 's satisfying the conditions (2.1), (2.2) and (2.3) by D .

In this lecture we use 6 simple behaviour model: homogeneous [6], cyclical [6], random [2], random with step [3], random with repetition [7] nad independent [5] ones. Let HOM , CYCL , RAN , STEP_p , REP_p and $\text{IND}_{p_1, \dots, p_n}$ denote them.

According to the homogeneous model the references are equivalent, that is

$$(2.4) \quad P\{r_1 = v_i\} = \frac{1}{n} \quad \text{and} \quad r_t = r_1 \quad (t = 2, 3, \dots : i = 1, \dots, n).$$

This formula is equivalent to the following definition:

$$(2.5) \quad \text{HOM}[\omega_k] = \begin{cases} \frac{1}{n}, & \text{if in } \omega_k \quad r_1 = r_2 = \dots = r_k \\ 0, & \text{otherwise.} \end{cases} \quad (k = 1, 2, \dots).$$

According to the cyclical model the step $v_i \rightarrow v_{i+1}$ ($v_{n+1} \equiv v_1$) has a probability 1, that is

$$(2.6) \quad P\{r_1 = v_i\} = \frac{1}{n} \quad \text{and} \quad P[r_{t+1} = v_{i+1}] = \begin{cases} 1, & \text{if } r_t = v_i, \\ 0, & \text{if } r_t \neq v_i \end{cases} \quad (t = 1, 2, \dots ; i = 1, \dots, n).$$

This formula is equivalent to the following definition:

$$(2.7) \quad \text{CYCL}[\omega_k] = \begin{cases} \frac{1}{n}, & \text{if in } \omega_k \quad \text{from} \quad r_t = v_i, \quad r_{t+1} = v_j \\ & \quad \text{follows} \quad j \equiv i + 1 \pmod{n} \\ 0, & \text{otherwise.} \end{cases} \quad (t = 1, 2, \dots).$$

According to the random model the references occur randomly, that is

$$(2.8) \quad P\{r_t = v_i\} = \frac{1}{n} \quad (t = 1, 2, \dots; i = 1, \dots, n).$$

This formula is equivalent to the following definition:

$$(2.9) \quad \text{RAN}[\omega_k] = \frac{1}{n^k} \quad (k = 1, 2, \dots, \omega_k \in N^k).$$

According to the random model with repetition the repetition has a probability p , and other references have a probability $\frac{1-p}{n-1}$:

$$(2.10) \quad P\{r_1 = v_i\} = \frac{1}{n}, \quad P\{r_t = v_i\} = \begin{cases} p, & \text{if } r_t = v_i, \\ \frac{1-p}{n-1}, & \text{if } r_t \neq v_i, \end{cases}$$

$$(t = 2, 3, \dots; i = 1, \dots, n)$$

This formula is equivalent to the following definition:

$$(2.11) \quad \text{REP}_p[\omega_k] = \frac{1}{n} \cdot p^f \left(\frac{1-p}{n-1}\right)^{k-f-1} \quad (k = 1, 2, \dots),$$

where f is the number of the repetitions in ω_k .

According to the random model with step [3] the step $v_i, v_{i+1} (v_{n+1} \equiv v_1)$ in ω_k has a probability p , and other references have a probability $\frac{1-p}{n-1}$:

$$(2.12) \quad P\{r_1 = v_i\} = \frac{1}{n}; \quad P\{r_t = i+1\} = \begin{cases} p, & \text{if } r_{t-1} = v_i, \\ \frac{1-p}{n-1}, & \text{if } r_{t-1} \neq v_i, \end{cases}$$

$$(t = 2, 3, \dots; i = 1, \dots, n).$$

This formula is equivalent to the following definition:

$$(2.13) \quad \text{STEP}_p[\omega_1] = \frac{1}{n}; \quad \text{STEP}[\omega_k] = \frac{1}{n} \cdot p^f \left(\frac{1-p}{n-1}\right)^{k-f-1} \quad (k = 1, 2, \dots),$$

where f is the number of the steps in ω_k .

According to the independent model [5] the reference to the page v_i has a probability p_i , that is

$$(2.14) \quad P\{r_t = v_i\} = p_i \quad (t = 1, 2, \dots).$$

This formula is equivalent to the following definition:

$$(2.15) \quad \text{IND}_{p_1, \dots, p_n}[\omega_k] = \prod_{i=1}^n (p_i)^{f_i},$$

where f_i is the number of the references to the page v_i .

Computer performance is characterized by the number of operations in a time unit: V.V is called the speed of the computer model and is determined by the formula

$$(2.16) \quad V = \liminf_{k \rightarrow \infty} \frac{1}{\sum_{\omega_k \in N^k} D[\omega_k] \frac{\pi[\omega_k]}{k}}.$$

If in (2.16) we have existence of the lim in addition to the \liminf , then this limit is denoted by V' .

Our aim is to determine the speed for various computer and program behaviour models.

3. The mathematical model of computers with paged memory

For the investigation of computers with paged memory we use the well-known model proposed by Bélády [2] in 1966.

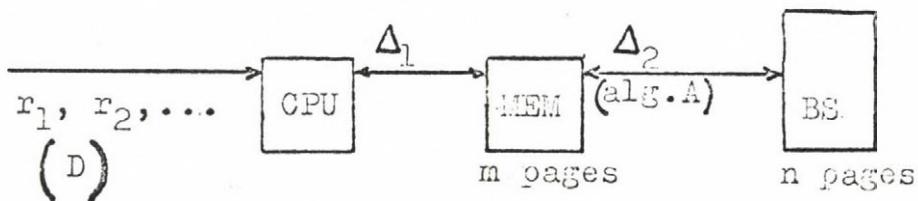


Fig. 1. The scheme of a computer with 2 level paged memory

The computer consists of a central processor unit (CPU), – an m -paged main memory (MEM) and an n -paged backing store (BS). The CPU has direct access to MEM-access time Δ_1 – while an indirect access to BS-access time $\Delta_1 + \Delta_2$. The paging is controlled by a demand paging algorithm. The set of demand paging algorithms by A

For this model speed V_p is [8]

$$(3.1) \quad V_p = \frac{1}{\Delta_1 + \Delta_2 \cdot C},$$

where C is the average cost of a reference, that is the page fault probability [5]. By definition

$$(3.2) \quad C = C(m, n, A, D) = \lim_{k \rightarrow \infty} \sup_{\omega_k \in N^k} \sum_{i=1}^k D[\omega_k] \left(\frac{\sum_{i=1}^k \delta_i}{k} \right)$$

where $A \in A$, $D \in D$,

$$(3.3) \quad \delta_i = \delta(i, m, n, \omega_T, A) = \begin{cases} 0, & \text{if } r_i \in S_t, \\ 1, & \text{if } r_i \notin S_t, \end{cases}$$

and S_t is the set of pages in MEM at time t . S_t is the set of pages in MEM at time t . S_t is called the memory state. If in (3.2) there exist a limit, then it is denoted by C .

4. General assertions on the speed of computers with paged memory

Lemma 1. ([7]) If $\Delta_1 > 0$, and $1 \leq m \leq n < \infty$, then

$$(4.1) \quad 0 = C(m, n, A, HOM) \leq C(m, n, A, D) \leq C(m, n, LRU, CYCL) = 1,$$

that is for the speed

$$(4.2) \quad \frac{1}{\Delta_1 + \Delta_2} = V_p(\Delta_1, \Delta_2, m, n, LRU, CYCL) \leq V_p(\Delta_1, \Delta_2, m, n, A, D) \leq V_p(\Delta_1, \Delta_2, m, n, A, HOM) = \frac{1}{\Delta_1}$$

holds.

Definition 1. ([9]). The demand paging algorithms, for which

$$(4.3) \quad \forall T_1, \forall T_2 \quad \sum_{i=1}^{T_1} \delta(i, m, n, \omega_{T_1}, A) = \sum_{i=1}^{T_2} \delta(i, m, n, \omega_{T_1}, \omega_{T_2}, A)$$

are called sequential [6]. The set of the sequential algorithms is denoted by B .

Lemma 2. If $1 \leq m \leq n < \infty$, then for every $B \in B$ and for every $D \in D$

$$(4.4) \quad C_{\inf} = \liminf_{\omega_k \in N^k} \sum_{i=1}^k D[\omega_k] \delta_k \leq C(m, n, B, D) \leq \limsup_{k \rightarrow \infty} \sum_{i=1}^k D[\omega_k] \delta_k.$$

Definition 2. Let $D \in D$ and N_+^k ($k = 1, 2, \dots; N_+^k \subseteq N^k$) be given. Denote a_k the sum $\sum_{\omega_k \in N_+^k} D[\omega_k]$. If

$$(4.5) \quad \lim_{k \rightarrow \infty} a_k = 0,$$

then we shall say, that the sequence N_+^k has zero limitdensity in N^k .

Lemma 3. ([7]). If for a given D there exist an m -tuple of pages μ_1, \dots, μ_m and μ_1, \dots, μ_m and $\epsilon > 0$, for which

(4.6) $\forall \omega_k \quad D[\omega_k \nu_i] \geq \epsilon. \quad D[\omega_k] \quad \text{holds, then the sequence } N_+^k \text{ has zero limit-density}$ in N^k , where N_+^k is the set of $\omega_k \in N^k$, for which $|S_t| = |S_t(m, \omega_k, B)| < m$.

Definition 3. Let ω_T be given. The sequences of lenght $(T + f)$ ($f = 0, 1, \dots$) identical to ω_T up to the T -th element, are called the bundle $\prod_f [\omega_T]$ with root ω_T and lenght f .

Definition 4. The average cost of a references C^{\prod} in a given bundle $\prod_f [\omega_T]$ is by definition

$$(4.7) \quad C = C^{\prod} (m, n, A, D, \omega_T) = \lim_{k \rightarrow \infty} \sum_{\omega_k \in \pi_{k-T} [\omega_T]} D[\omega_k] \delta_k,$$

where $D^{\prod} [\omega_k]$ is the probability of sequence ω_k ($\omega_k \in \pi_{k-T} [\omega_T]$) within the bundle, that is

$$(4.8) \quad D^{\prod} [\omega_k] = \frac{D[\omega_k]}{\sum_{\omega_k \in \pi_{k-T} [\omega_T]} D[\omega_k]}.$$

Lemma 4. ([7]). Let N_+^k denote the set of ω'_k not belonging to any bundle, which has a cost C^{\prod} . If the sequence N_+^k , then $C(m, n, B, D)$ exists and is C^{\prod} .

5. Theorems on the speed of computers with paged memory

Theorem A. (Belady, 1966) [2]. If L is a nonlookahead demand paging algorithm, then

$$(5.1) \quad C(m, m, L, RAN) = \frac{n-m}{n}.$$

Theorem B. (Aho, Denning, Ullman 1971/([5])). If $1 \leq m \leq n < \infty$.

then

$$(5.2) \quad C'(m,n,\text{OPT},\text{IND}) = \frac{\sum_{i=m}^n p_i^2}{\sum_{i=m}^n p_i}$$

where OPT is the optimal paging algorithm, always replacing the page of S_t with minimal p_i [5].

Theorem C. (Stoyan, 1975/([8])). If $1 \leq m \leq n < \infty$, then

$$(5.3) \quad C'(m,n,\text{REF}_a,\text{RAN}) = \frac{n-m}{n+a} \quad (a = 0, 1, \dots, m-1),$$

where REF_a is a lookahead algorithm, which knows a references ahead, and holds required pages in the memory if possible, and chooses randomly among the others.

Theorem 1. ([7]). If $1 \leq m \leq n < \infty$, and $0 \leq a \leq m$, then

$$(5.4) \quad C'(m,n,\text{REF}_a,\text{REP}_p) = \frac{(n-m)(1-p)}{n-1 + [\min/a, m-1]1-p + [\max/0, a-m+1]\left(1-\frac{1-p^{m-1}}{n-1}\right)(1-p)}$$

Theorems A and B follow from theorem 1 (in cases $a = 0$, $p = \frac{1}{n}$ and $0 \leq a \leq m-1$, $p = \frac{1}{n}$).

Theorem 2. ([7]). If $1 \leq m \leq n < \infty$, then

$$(5.5) \quad C'(m,n,\text{PP}_b,\text{RAN}) = \frac{n-m}{n} \left(\frac{n-1}{n}\right)^b \quad (b = 0, 1, \dots)$$

where PP_b is a lookahead algorithm, which knows at time t the next b references, differing from r_t and each other, and hold these pages if possible, in the memory, and chooses randomly among the others.

In case $b = 1$, $m = 2$, $n = 3$, it follows from theorem 2. the partial resolution of the problem, investigated by Bélády in 1966, namely $C'(2,3,\text{MIN},\text{RAN}) = \frac{2}{9}$.

Theorem 3. ([7]). If $a \geq 0$, then

$$(5.6) \quad C'(2,3,\text{REF}_a,\text{REP}_p) = \frac{1-p}{2 + [\min/a, 1]/1 - p/ + \text{sign}[\max/0,a-1]p/1 - p^{a-1}/}$$

From this theorem it follows (in the case $a \rightarrow \infty$, when $\text{REF}_a \rightarrow \text{MIN}$), that

$$(5.7) \quad C'(2,3,\text{MIN},\text{REP}_p) = \frac{1-p}{3}.$$

6. Mathematical model of computers with interleaved memory

We investigate the following model of computers with interleaved memory due to V.E.Vulihman [10]:

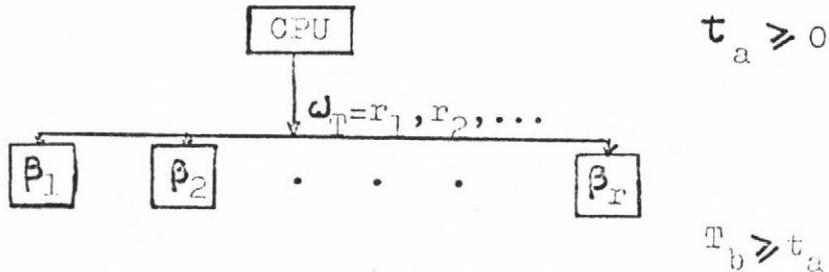


Fig. 2. Scheme of computers with interleaved memory

The computer consists of a central processor unit (CPU) and models of memory β_1, \dots, β_r . The set of modules is denoted by B . The elements of ω_T are generated by the CPU requiring $t_a \geq 0$ time per element. A request to a module reserves that module for a time $T_b > t_a$ during this time other requests can't be served by this module. If the module being requested is occupied, then the generation of ω_T will be suspended until the module is free.

The speed of this module is denoted by V_i .

7. General assertions on the speed of computers with interleaved memory

Hellerman in his book [6], Bokova and Tzaturyan in their paper [11] proved the following assertions.

Lemma 5. ([6]). *If $T_b > t_a \geq 0$, then for every $D \in D$ and for every $r \geq 1$*

$$(7.1) \quad \frac{1}{T_b} = V'_i(t_a, T_b, r, \text{HOM}) \leq V_i(t_a, T_b, r, D) \leq V_i(t_a, T_b, r, \text{CYCL}) = \frac{1}{T} \min(r, \frac{T_b}{t_a}),$$

where HOM and CYCL are the homogeneous and cyclical behaviour models.

Definition 5. Let $\rho_i (\rho_0 = 0)$ denote the processing time of ω_T up to i -th element. Then the increment due to the i -th element is $\sigma_i = \rho_i - \rho_{i-1}$.

Example. For every $\omega_2 \in N^2$

$$(7.2) \quad \sigma_1 = T_b + t_a, \quad \sigma_2 = \begin{cases} T_b, & \text{if } r_2 = r_1 \\ t_a, & \text{if } r_2 \neq r_1 \end{cases}$$

Lemma 6. ([11]). If $T_b > t_a \geq 0$, then

$$(7.3) \quad \liminf_{k \rightarrow \infty} \frac{1}{\sum_{\omega_k \in N^k} D[\omega_k] \sigma_k} \leq V_i(t_a, T_b, r, D) \\ \leq \limsup_{k \rightarrow \infty} \frac{1}{\sum_{\omega_k \in N^k} D[\omega_k] \sigma_k}$$

Lemma 7. ([11]). If $T_b > t'_a > t''_a \geq 0$, then

$$(7.4) \quad V_i(t'_a, T_b, r, D) \leq V_i(t''_a, T_b, r, D).$$

Lemma 8. ([11]). If $T_b > t_a \geq 0$, and $r' > r''$, then

$$(7.5) \quad V_i(t_a, T_b, r', D) \geq V_i(t_a, T_b, r'', D).$$

8. Theorems on the speed of computers with interleaved memory

Theorem 4. ([7]). If $T_b > t_a \geq 0$, then for $r \geq 1$ $V'_i(t_a, T_b, r, \text{RAN}) \leq$

$$(7.6) \quad \leq \frac{1}{t_a \sum_{i=1}^r \sum_{j=2}^i p_{i,j} + \left(\sum_{i=1}^r p_{i,1} \right) \left[\sum_{k=0}^{r-1} (\max(t_a, T_b - k \cdot t_a) \sum_{j=k+1}^r \frac{p_j}{j}) \right]}$$

and the equality holds

- a.) if $t_a = 0$, then for $r = 1$;
- b.) if $\frac{T_b}{2} \leq t_a \leq T_b$, then for $r \geq 1$;
- c.) if $0 < t_a < \frac{T_b}{2}$, then for $r = 1, 2$.

In the formula (7.6)

$$(7.7) \quad p_i = \frac{r(r-1)\dots(r-i+1)}{r^{i+1}} \quad i(1 \leq i \leq r),$$

and

$$(7.8) \quad p_{i,j} = \frac{p_i}{\sum_{i=1}^r i \cdot p_i} \quad (1 \leq i \leq r, \quad 1 \leq j \leq i).$$

The following corollaries follow from theorem 4. as special cases.

Corollary 1 (Hellerman, 1967.) ([6]). If $t_a = 0$ and $r \geq 1$, then

$$(7.9) \quad V'_i(0, T_b, r, \text{RAN}) = \frac{r}{T_b} \sum_{i=1}^r i \cdot p_i.$$

Burnett, Coffman [3] and Stone [12] proved a more general assertion.

Theorem D. (Burnett, Coffman, Stone 1974.) [3.12].

If $t_a = 0$ and $r \geq 1$, then

$$(7.10) \quad \begin{aligned} V'_i(0, T_b, r, \text{STEP}_p) &= \\ &= \frac{2}{T_b} \sum_{k=1}^r \sum_{j=0}^{k-1} (k-j-1)_p \cdot j \cdot \left(\frac{1}{n-1}\right)^{k-j-1} \cdot C_{n-j, k-j}, \end{aligned}$$

where

$$(7.11) \quad C_{n,k} = \sum_{j=0}^{k-1} [(-1)^j \binom{k-1}{j} (n-j-1)(n-j-2)\dots(n-k+1)].$$

Corollary 2. If $\frac{T_b}{2} \leq t_a \leq T_b$ and $r \geq 1$, then

$$(7.12) \quad V'_a(t_a, T_b, r, \text{RAN}) = \frac{1}{\frac{1}{r} T_b + (1 - \frac{1}{r}) t_a}.$$

Corollary 3. If $T_b > t_a \geq 0$, then

$$(7.13) \quad V'_b(t_a, T_b, 2, \text{RAN}) = \frac{1}{\frac{1}{3}t_a + \frac{1}{2}T_b + \frac{1}{6}\max(t_a, T_b - t_a)}.$$

On the base of the formula (7.9) it is not easy to estimate the order of $V'_i(0, T_b, r, \text{RAN})$, therefore the following theorems are interesting.

Theorem E. (Hellerman, 1967)([6]). If $1 \leq r \leq 45$, then

$$(7.15) \quad 0,96 \cdot r^{0,56} \leq V'_i(0, T_b, r, \text{RAN}) \leq 1,04 \cdot r^{0,56}.$$

Theorem F. (Vulihman, 1972.)([10]). If $r \geq 1$, then

$$(7.16) \quad V'_a(0, T_b, r, \text{RAN}) \leq \sqrt{2\Pi r}.$$

We proved the following more general theorems.

Theorem 5. ([13]). If $t_a = 0$ and $r \geq 1$, then

$$(7.17) \quad \frac{1}{T_b} (\sqrt{\frac{\Pi r}{2}} - 1) \leq V'_i(0, T_b, r, \text{RAN}) = \frac{r! \sum_{k=0}^{r-1} \frac{r^k}{k!}}{T_b \cdot r'} \frac{1}{T_b} (\sqrt{\frac{\Pi r}{2}} + 1).$$

In our paper [7] we used a simple direct proof. Using a result due to G. Szegő [14] we can proof a formula with a smaller additive constant, which is exact.

Theorem G. (Szegő, 1928)([14]). If q is a nonnegative integer number, then

$$(7.18) \quad \frac{1}{2} e^q = 1 + \frac{q}{1!} + \frac{q^2}{2!} + \dots + \frac{q^q}{q!} \Theta_q,$$

where $\Theta_0 = \frac{1}{2}$ and Θ_q thends monotonically to $\frac{1}{3}$ as $q \rightarrow \infty$.

Theorem 6. If $t_a = 0$ and $r \geq 1$, then

$$(7.19) \quad V'_i(0, T_b, r, \text{RAN}) = \frac{1}{T_b} (\sqrt{\frac{\Pi r}{2}} - \frac{1}{3} + \rho_r),$$

where ρ_r tends monotonically to zero as $r \rightarrow \infty$ and

$$(7.20) \quad \rho_1 = \frac{4}{3} - \sqrt{\frac{\Pi}{2}} \approx 0,08 \text{ and } \rho_2 = \frac{11}{6} - \sqrt{\Pi} \approx 0,06.$$

It seems a hard but resolvable problem to estimate the order of expression in Coffman's theorem, as a function of p .

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Összegolaló

Lapozott és atlapolt memóriájú számítógépek sebessége

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Az előadásban definiáljuk a számítógépek teljesítményét jellemző mennyiséget, a sebességet. Ezt a sebességet megadjuk a hardwareő és a programviselkedési paraméterek függvényében a lapozott memóriáju számítógépek Bélády-féle és az átlapolt memóriáju számítógépek Vulihman-féle modelljére.

Kulcsszavak és kifejezések: számítógépek teljesítménye, igénye szerinti, lapozás, átlapolt memória, programviselkedés.

РЕЗЮМЕ

О скорости ЭВМ со страничной и блочной памятью

А. Ивани и И. Катаи

Резюме: определена мера /скорость/ производительности математических моделей ЭВМ и задана эта скорость, как функция параметров аппаратуры и поведения программ для математической модели ЭВМ со страничной памятью /предложенной Л.А. Белади/ и математической модели ЭВМ с блочной памятью /предложенной В.Е. Вулихманом/.

Ключевые слова и выражения: производительность вычислительных систем, страничная память, блочная память, поведение программ.